IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

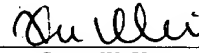|	
In re application of: Stepan Sokolov	Attorney Docket No.: SUN1P839/P6719
Application No.: 09/939,310	Examiner: GORDON, CARLENE M.
Filed: August 24, 2001	Group: 2124
Title: FRAMEWORKS FOR GENERATION OF JAVA MACRO INSTRUCTIONS FOR INSTANTIATING JAVA OBJECTS	Confirmation No. 2836

CERTIFICATE OF MAILING
I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on January 6, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____
Susan W. Xu

INFORMATION DISCLOSURE STATEMENT
BEFORE FINAL ACTION OR NOTICE OF ALLOWANCE
(37 CFR §§ 1.56 AND 1.97(c))

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed after the mailing date of the first Office Action on the merits, or after three months of the filing date of this application, whichever event occurred last, but it is believed before the mailing date of either: (i) a final action under §1.113 or (ii) a notice of allowance under §1.311, whichever occurs first.

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Accompanying this Information Disclosure Statement is

☐ a statement as specified in 37 CFR 1.97(e); or

☒ the fee set forth in 37 CFR 1.17(p).

The undersigned hereby states:

☐ that each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application no more than three months prior to the filing of the Information Disclosure Statement; or

☐ that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

If fees are due, enclosed is our Check No. 25015 for $180.00 in payment of the Information Disclosure Statement Fee. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P839).
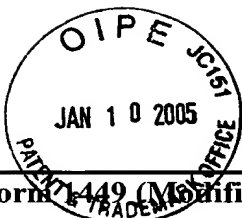
Respectfully submitted,
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Form 1449 (Modified)	Atty. Docket No. SUN1P839/P6719	Application No.: 09/939,310
Information Disclosure Statement By Applicant	Applicant: Stepan Sokolov	
(Use Several Sheets if Necessary)	Filing Date August 24, 2001	Group 2124

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	2002/0170043 A1	11/14/02	Bagley et al.			02/23/01
	B	4,086,626	04/25/78	Chung			06/07/76
	C	4,199,811	04/22/80	Borgerson et al.			09/02/77
	D	5,838,980	11/17/98	Guillen et al.			06/25/97
	E	5,872,978	02/16/99	Hoskins			12/19/96
	F	5,893,084	04/06/99	Morgan et al.			10/04/96
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	H	5,920,720	07/06/99	Toutonghi et al.			02/25/97
	I	6,072,951	06/06/00	Donovan et al.			10/15/97
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	N	6,151,618	11/21/00	Wahbe et al.			06/18/97
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	V	6,658,421 B1	12/02/03	Seshadri			02/14/00

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	Translation No
	W	EP 0 996 059 A2	04/26/00	EPO				
	X	WO01/22213 A2	03/29/01	PCT				
	Y	WO02/077807 A1	10/03/02	PCT				
	Z	WO02/41145 A2	05/23/02	PCT				
Examiner				Date Considered				

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form 1449 (Modified)	Atty. Docket No. SUN1P839/P6719	Application No.: 09/939,310
Information Disclosure Statement By Applicant	Applicant: Stepan Sokolov	
(Use Several Sheets if Necessary)	Filing Date August 24, 2001	Group 2124

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	AA	Lambright H D, "Java Bytecode Optimizations" Proceedings of IEEE Compcon '97. San Jose, Feb. 23-26, 1997, pages 206-210, XP-000751784
	BB	Accomazzo E et al., "Integrating intermediate Code Optimization with Retargetable Code Generation" Microprocessing and Microprogramming, Elsevier Science Publishers, BV. Amsterdam, NL, vol. 30, No. 1/5, August 1, 1990, pages 475-481, XP000141684
	CC	McNeley KJ et al., "Emulating a Complex Instruction Set Computer With a Reduced Instruction Set Computer," IEEE Micro, IEEE Inc. New York, US, vol. 7, no. 1, February 1987, Pages 60-71. XP-000827611
	DD	Jean-Paul Billon, "JEFFWEG4 (J Executable File Format), Release 1.0 Achievements, 9/29/2000," J Consortium Jeff Working Group, pages 1-24. XP-002208358
	EE	Microsoft Press Computer Dictionary Third Edition, pgs. 294-295
	FF	Case B: "Implementing The Java Virtual Machine *Java's Complex Instruction Set Can Be Built in Software or Hardware*," Microprocessor Report, Vol.10, no. 4, March 25, 1996, pages 12-17. XP-000987276
	GG	Chang et al., "EJVM: an economic Java run-time environment for embedded devices," Software Practice & Experience, 2001, John Wiley & Sons, Ltd., Vol. 31, pages 129-146. XP-000987539
	HH	Gosling et al., "The Java™ Language Specification", August 1, 1996, pages 215-236. XP 002042923
	II	Zhenyu Qian, "A formal Specification of Java™ Virtual Machine Instructions," Technical Report, University Bremen; November 1997, pages 1-32. XP-002255760
	JJ	Piumarta et al., "Optimizing Direct Threaded Code by Selective Inlining," Association for Computing Machinery, Vol. 33, No. 5, pp. 291-300, May 1, 1998.XP-000766278
	KK	Tommesani, "Programming models"
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.